Dear Stephen,

I am Stephen Justin from Sydney Australia. I rang you in your morning the 16th July. It was wonderful to speak with you at length and learning about MPE and your Forth background.

I spoke about our Aussie single board computer called the TEC-1

Am wanting to learn and develop forth apps on it with a “tethered” method. The ram is limited at between 2 to 14k . The ROM is 4k split in half with a switch so each image is 2k. The circuit can be modified to fix this to 4k, but other address lines will need to move.

The current group is; <https://www.facebook.com/groups/623556744820045/>

Also the blue “learn more” button on the FB page sends to a spreadsheet with links, <https://docs.google.com/spreadsheets/d/196W4LSZB3WT3BBj0CI_23vvdlKwxeybq7q1GpcKLlhA/edit#gid=0>

The old group is; <https://www.facebook.com/groups/AusVintage/>

Other links

<https://github.com/tec1group/TEC-1>

<https://github.com/jhlagado/TEC-1>

<https://github.com/jhlagado/firth>

my git; <https://github.com/SteveJustin1963> full of learning ideas for forth

Circuit is in in issue 14;

<https://github.com/SteveJustin1963/tec-BOOKS/blob/master/TE/Mag/talking_electronics_14.pdf>

I made a cct here as well,; <https://easyeda.com/editor#id=f83d6e4345f549a7bd7ee7b231e8b4e3>

Looking at your web page the compiler is

|  |  |  |
| --- | --- | --- |
| Z80/Z180/84Cxx | Excellent 8 bit performance; easy to expand memory, interesting EZ80 cores | Order code - XC180/60 |

Could not find a link to download XC180/60.

Ill prepare a working TEC-1 with a serial port on it for you.

Anyway look forward to your help and training package

Regards

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